SAMSUNG
ELECTRONICS

DATE : 07. Feb. 2012

SAMSUNG TFT-LCD

MODEL **: LTA480HN01**

The Information Described in this Specification is Preliminary and can be changed without prior notice

LCD Business

Samsung Electronics Co . , LTD.

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Revision History

Date	Rev. No	Page	Summary
07. Feb. 2012	000	all	First issued

General Description

Description

LTA480HN01 is a color active matrix liquid crystal display (LCD) that uses amorphous silicon TFT (Thin Film Transistor) as switching components. This model is composed of a TFT LCD panel, a driver circuit and a back light unit.

The resolution of a 48.0" is 1920 x 1080 and this model can display up to 16.7 Million colors with wide viewing angle of 89° or higher in all directions. This panel is intended to support applications to provide a excellent performance for Flat Panel Display such as Home-alone Multimedia TFT-LCD TV and High Definition TV.

Features

- RoHS compliance (Pb-free)
- High contrast ratio & aperture ratio with wide color gamut
- SVA(Super Vertical Align) mode
- Wide viewing angle (±178°)
- High speed response
- FHD resolution (16:9)
- Low Power consumption
- Edge Type LED (Light Emitted Diode) BLU
- DE (Data Enable) mode
- 2ch LVDS (Low Voltage Differential Signaling) interface (2pixel/clock)

General Information

Items	Specification	Unit	Note
Module Size	1078.6 (H) x 626.0 (V)	mm	±1.0mm
	32.4 (Dmax)		Cover-converter
Weight	13.2 (max)	kg	
Pixel Pitch	0.5490(H) x 0.5490(W)	mm	
Active Display Area	1054.08 (H) x 592.92 (V)	mm	
Surface Treatment	Anti-glare	-	
Display Colors	8bit – 16.7M	Colors	
Number of Pixels	1920 x 1080	Pixel	
Pixel Arrangement	RGB vertical stripe	-	
Display Mode	Normally Black	-	
Luminance of White	350 (Typ.)	cd/m^2	

1. Absolute Maximum Ratings

If the condition exceeds maximum ratings, it can cause malfunction or unrecoverable damage to the device.

Item		Symbol	Min.	Max.	Unit	Note
Power Supply Voltage		V_{DD}	GND-0.3	13.2	V	(1)
Storage temperature		T_{STG}	-20	60	℃	(2)
Glass surface temperature (Operation)	Center	T_{OPR}	0	50	℃	(2),(5)
	T. Uniformity	$\triangle T$	-	10	℃	
Shock (non - operating)		S_{nop}	-	50	G	(3)
Vibration (non - operating)		V_{nop}	-	1.5	G	(4)

Note (1) Ta= 25 ± 2 °C
   (2) Temperature and relative humidity range are shown in the figure below.
      a. 90 % RH Max. (Ta ≤ 39 °C)
   b. Relative Humidity is 90% or less. (Ta > 39 °C)
      c. No condensation
   (3) 11ms, sine wave, one time for $\pm X, \pm Y, \pm Z$ axis
   (4) 10-300 Hz, Sweep rate 10min, 30min for X,Y,Z axis

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(5) Definition of test point

5mm

5mm

LCD Module (Active)
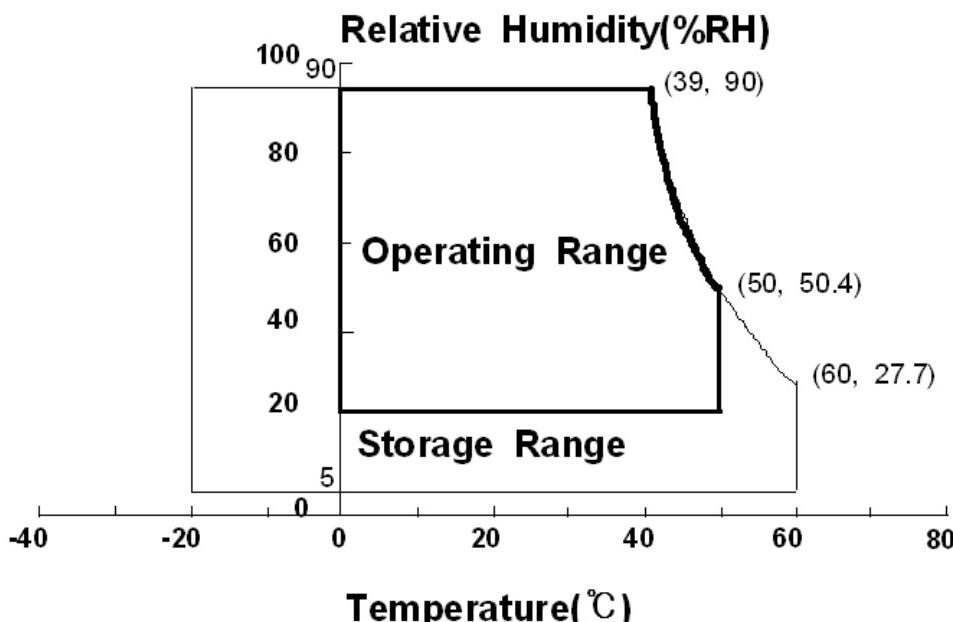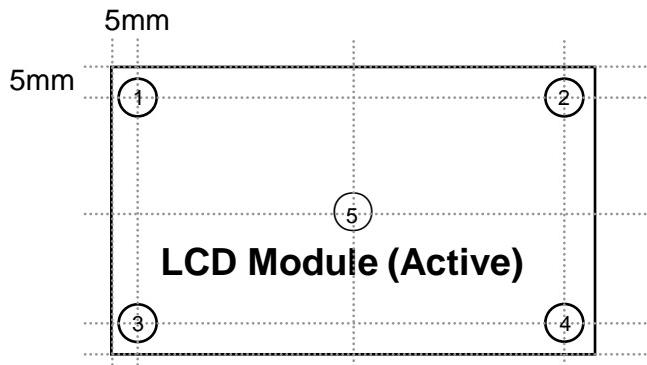
$\triangle$T should be less than 10 $°C$ ($\triangle$T = | T_{OPR} – T_{MAX}|)

T_{OPR} : Temperature of the center of the glass surface (Test point 5)
T1~ T4 : Temperature of each edge of the glass surface
T_{MAX} : The highest temperature of the glass surface

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2. Optical Characteristics

The optical characteristics should be measured in a dark room or equivalent.
Measuring equipment : TOPCON RD-80S, TOPCON SR-3 ,ELDIM EZ-Contrast

(Ta = 25 $\pm$ 2°C, VDD=12.0V, fv=60Hz, f_{DCLK}=148.5MHz, LED Current = 100 mA)

Item		Symbol	Condition	Min.	Typ.	Max.	Unit	Note
Contrast Ratio (Center of screen)		C/R		3,000	4,000	-		(1) SR-3
Response Time	G-to-G	Tg		-	12	-	msec	(3) RD-80S
Luminance of White (Center of screen)		Y_L		300	350	-	cd/m^2	(4) SR-3
Color Chromaticity (CIE 1931)	Red	Rx	Normal θ**L,R**=0 θ**U,D**=0 Viewing Angle	TYP. -0.03	0.649	TYP. +0.03		(5),(6) SR-3
		Ry			0.334			
	Green	Gx			0.306			
		Gy			0.608			
	Blue	Bx			0.150			
		By			0.061			
	White	Wx			0.280			
		Wy			0.290			
Color Gamut		-		-	70	-	%	(5) SR-3
Color Temperature		-		-	10,000	-	K	
Viewing Angle	Hor.	θ_L	C/R≥10	75	89	-	Degree	(6) EZ-Contrast
		θ_R		75	89	-		
	Ver.	θ_U		75	89	-		
		θ_D		75	89	-		
Brightness Uniformity (9 Points)		B_{uni}		-	-	30	%	(2) SR-3

- Test Equipment Setup

   The measurement should be executed in a stable, windless and dark room between
   40min and 60min after lighting the back light at the given temperature for stabilization
   of the back light. This should be measured in the center of screen.

   Environment condition : Ta = 25 $\pm$ 2 °C

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Photo detector	Field
SR-3	1°
RD-80S	2°

Photo detector

Field

SR-3 : 50㎝
RD-80S : 50㎝
EZ-Contrast:0㎝

TFT - LCD Module

LCD Panel

The center of the screen

- Definition of test point

	320	960	1600
180	⑨	⑧	⑦
540	⑥	⑤	④
900	③	②	①

Active Area
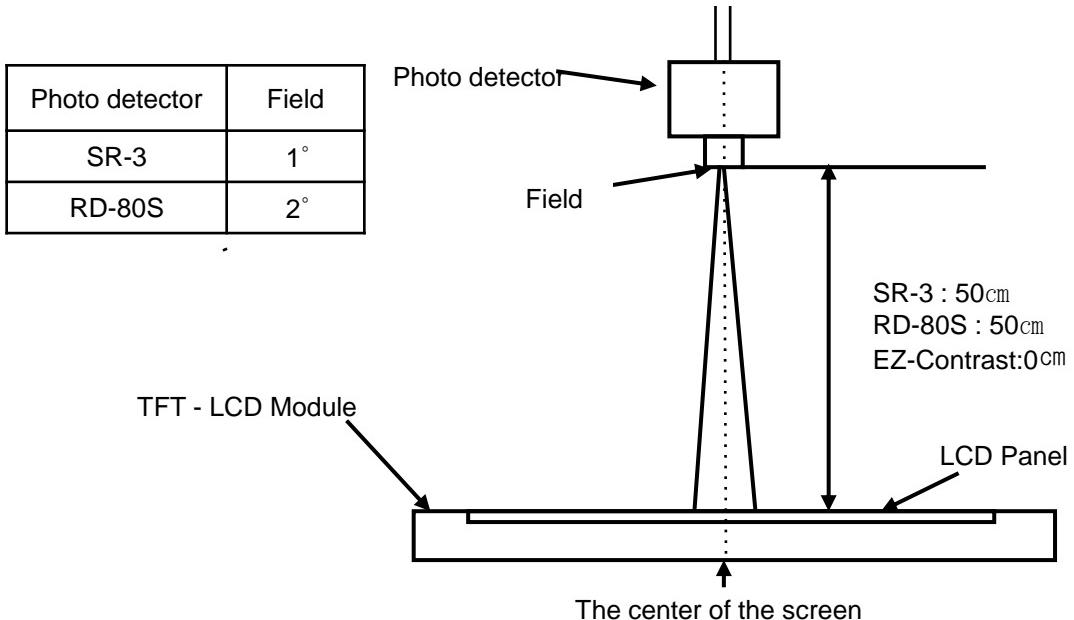
◯ Test Point
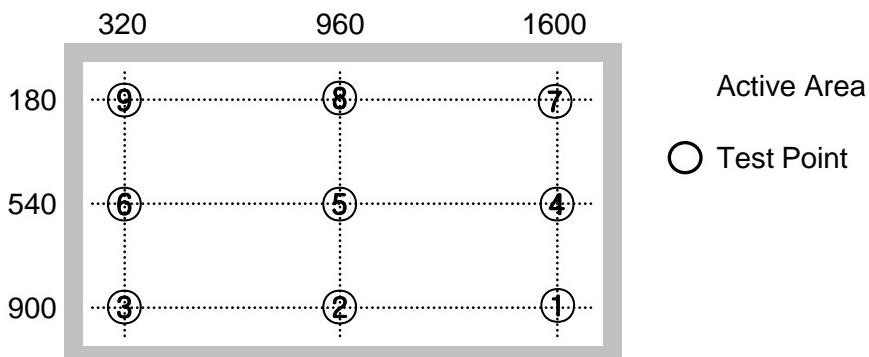
Note (1) Definition of Contrast Ratio (C/R)
: Ratio of gray max (Gmax) & gray min (Gmin) at the center point ⑤ of the panel

$$C/R = \frac{G\max}{G\min}$$

Gmax : Luminance with all pixels white
Gmin : Luminance with all pixels black

Note (2) Definition of 9 points brightness uniformity (Test pattern : Full White)

$$Buni = 100 * \frac{(B\max - B\min)}{B\max}$$

Bmax : Maximum brightness
Bmin : Minimum brightness

Note (3) Definition of Response time : Sum of Tr, Tf

Display data	Black (data off)	White (data on)	Black (data off)
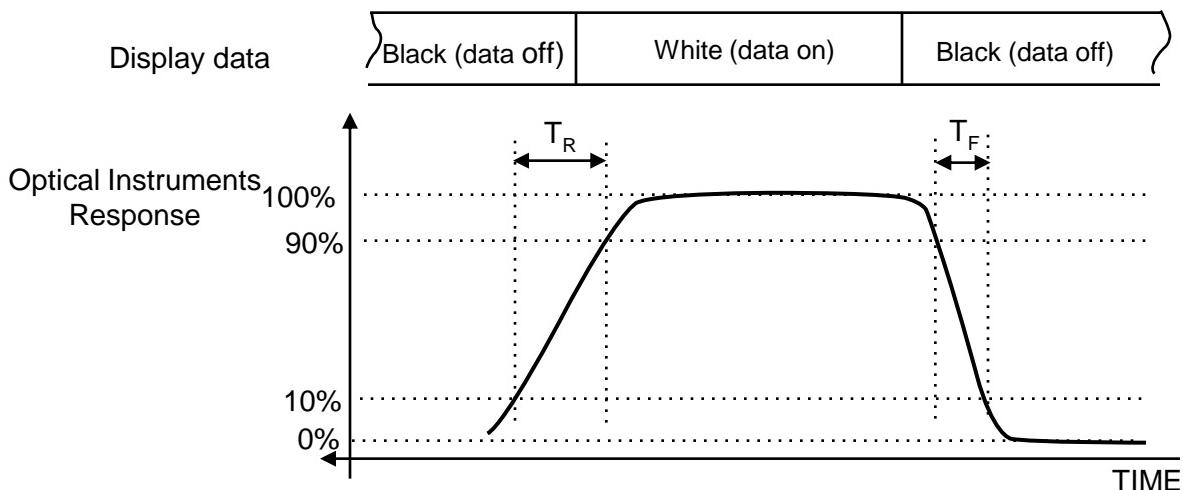
※ G-to-G : Average response time between Gray to Gray (Scale)

Note (4) Definition of Luminance of White : Luminance of white at center point ⑤

Note (5) Definition of Color Chromaticity (CIE 1931)
Color coordinate of Red, Green, Blue & White at center point ⑤

Note (6) Definition of Viewing Angle
: Viewing angle range (C/R ≥10)

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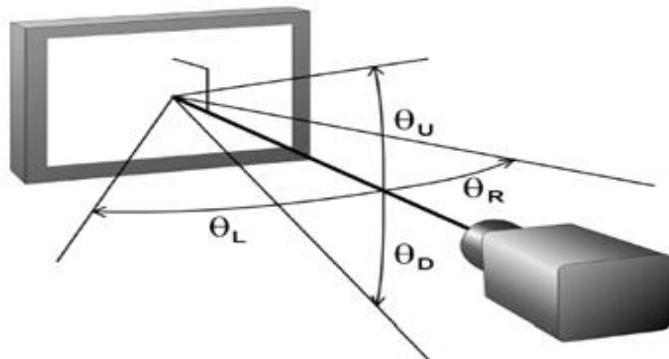
3. Electrical Characteristics

3.1 TFT LCD Module

The connector for display data & timing signal should be connected.

Ta = 25°C ± 2 °C

Item		Symbol	Min.	Typ.	Max.	Unit	Note
Voltage of Power Supply		V_{DD}	10.8	12.0	13.2	V	(1)
Current of Power Supply	(a) Black	I_{DD}	-	500	650	mA	(2),(3)
	(b) White		-	500	650	mA	
	(c) Sub V-Stripe		-	800	1000	mA	
Vsync Frequency		f_V	48	60	65	Hz	
Hsync Frequency		f_H	60	67.5	70	kHz	
Main Frequency		Fdclk	130	148.5	160	MHz	
Rush Current		I_{RUSH}	-	-	5	A	(4)

Note (1) The ripple voltage should be controlled under 10% of V_{DD}.
 (2) f_V=60Hz, f_{DCLK} =148.5MHz, V_{DD} = 12.0V, DC Current.
  (3) Power dissipation check pattern (LCD Module only)
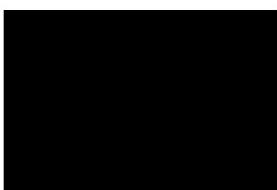
a) Black Pattern   b) White Pattern   c) Sub V-stripe
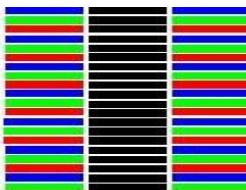
(4) Measurement Conditions

T_{RUSH}=470μs
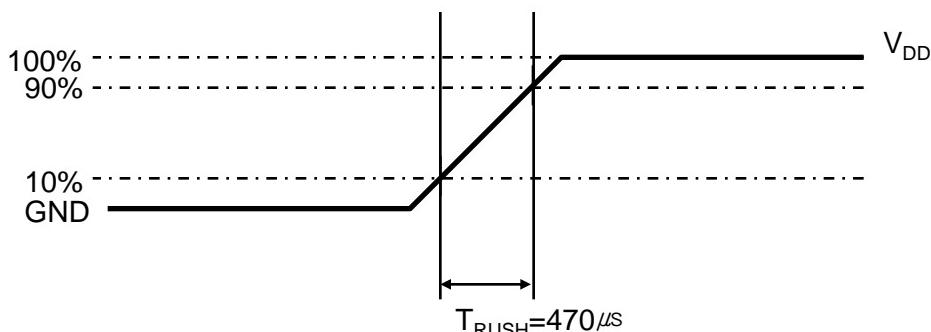
Rush Current I_{RUSH} can be measured when T_{RUSH}. is 470μs.

3.2 Back Light Unit

The back light unit contains Edge type White LEDs (Light Emitting Diode)

Ta=25 ± 2°C
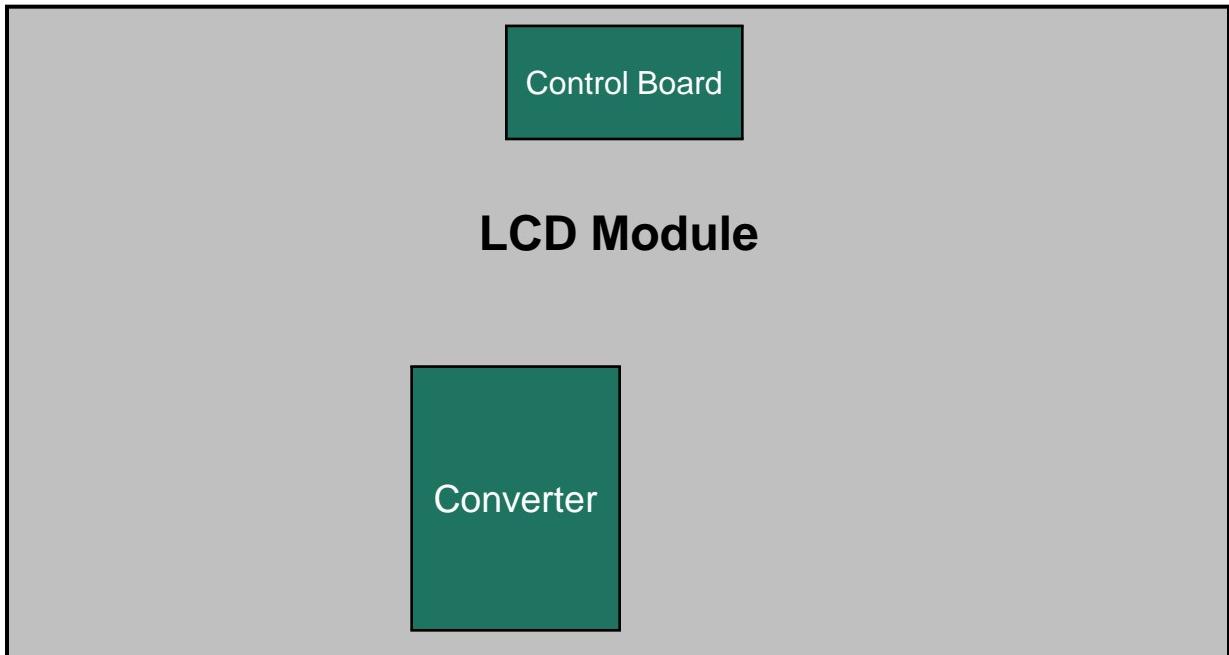
Item	Symbol	Min.	Typ.	Max.	Unit	Note
Operating Life Time	Hr	30,000	-	-	Hour	(1)

Note (1) It is defined as the time to take until the brightness reduces to 50% of its original

value.

[Operating condition : Ta = 25±2℃, For single lamp only.]

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3.3 Converter Input Condition & Specification

Items	Symbol	Conditions	Specifications			Unit	Note
			Min.	Typ.	Max.		
Input Voltage	Vin	-	22	24	26	V	Ta=25±2 °C
Input Current	I_{RUSH}	Vin=24.0V Vdim =3.3V	-	-	4.5	A	
Output Current	$I_{O,MAX}$	Vin = 24.0V V dim =3.3 V	95	100	105	mArms	
Backlight On/Off	ON	Vin=24.0 V	2.4	-	5.5	V	
	OFF	Vin=24.0 V	0	-	0.8		
Dimming Range	$V_{\_DIM}$	Vin :22~26V	0	-	3.3	V	
Dimming Duty Output	D max	Vin=24V Dim:3.3V	100	-	-	%	
	D min	Vin=24V Dim:0V	1	-	-		
Dimming Frequency	F_{PWM}	Vin=24.0 V	140	150	160	Hz	
External Dimming Duty Range	EX_Dim	Min	1	-	100	%	
External Dimming Frequency Range	F_{EX_PWM}	Vin=22.0~26.0 V	95	-	200	Hz	
External Dimming Signal Level	V_{PWM}	High (ON)	3.0	-	5.5	V	
		Low (Off)	0	-	0.8		

Note) Power Consumption is measured when 350 [cd/m] of luminance which is the typical luminance.

(1) All data is measured after 120min warm-up.

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4. Input Terminal Pin Assignment

4.1. Input Signal & Power   Connector : FI-RE51S-HF (JAE)

Pin	Symbol	Description	Pin	Symbol	Description
1	12V	DC power supply	26	RE[0]P	Even LVDS Signal +
2	12V	DC power supply	27	RE[1]N	Even LVDS Signal -
3	12V	DC power supply	28	RE[1]P	Even LVDS Signal +
4	12V	DC power supply	29	RE[2]N	Even LVDS Signal -
5	12V	DC power supply	30	RE[2]P	Even LVDS Signal +
6	NC	NOTE1	31	GND	Ground
7	GND	Ground	32	ROCLK-	Even LVDS Clock -
8	GND	Ground	33	ROCLK+	Even LVDS Clock +
9	GND	Ground	34	GND	Ground
10	RO[0]N	Odd LVDS Signal -	35	RE[3]N	Even LVDS Signal -
11	RO[0]P	Odd LVDS Signal +	36	RE[3]P	Even LVDS Signal +
12	RO[1]N	Odd LVDS Signal -	37	NC	NOTE1
13	RO[1]P	Odd LVDS Signal +	38	NC	NOTE1
14	RO[2]N	Odd LVDS Signal -	39	GND	Ground
15	RO[2]P	Odd LVDS Signal +	40	NC	
16	GND	Ground	41	NC	
17	ROCLK-	Odd LVDS Clock -	42	NC	NOTE1
18	ROCLK+	Odd LVDS Clock +	43	NC	
19	GND	Ground	44	NC	
20	RO[3]N	Odd LVDS Signal -	45	GND	
21	RO[3]P	Odd LVDS Signal +	46	NC	
22	NC	NOTE1	47	NC	
23	NC	NOTE1	48	NC	NOTE1
24	GND	Ground	49	NC	
25	RE[0]N	Even LVDS Signal -	50	NC	
			51	NC	NOTE1

Note1) No Connection: These PINS are used only for SAMSUNG. (DO NOT CONNECT)

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PCB

▼

Pin No. 1   Pin No. 51

#1   #51

#1   #51
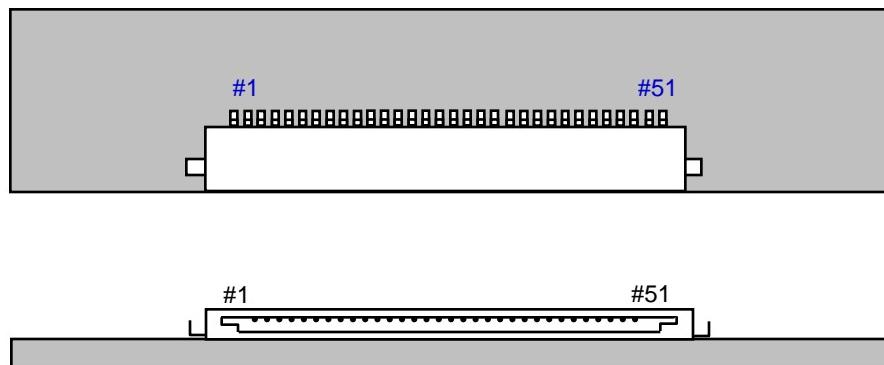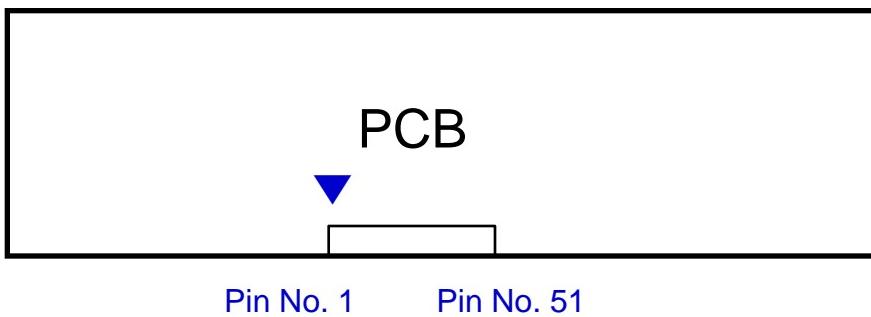
**Fig. Connector diagram**

a. Power GND pins should be connected to the LCD's metal chassis.
b. All power input pins should be connected together.
c. All NC pin should be separated from other signal or power.

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4.2. Converter Input Pin Configuration

Pin No.	Pin Configuration(FUNCTION)
	Master
1~5	24 V
6~10	GND
11	No connection
12	Backlight On /Off [ON:2.4 – 5.5 V, OFF: -0.3 - 0.8 V]
13	Dimming Control [0V:Min, 3.3V:Max] *Note(1)
14	External PWM [1~100%] *Note(1)

Note(1) If use Dimming Control, Pin 14 Must be N.C
          If use External PWM, Pin 13 Must be N.C

4.3. Converter Input Power Sequence
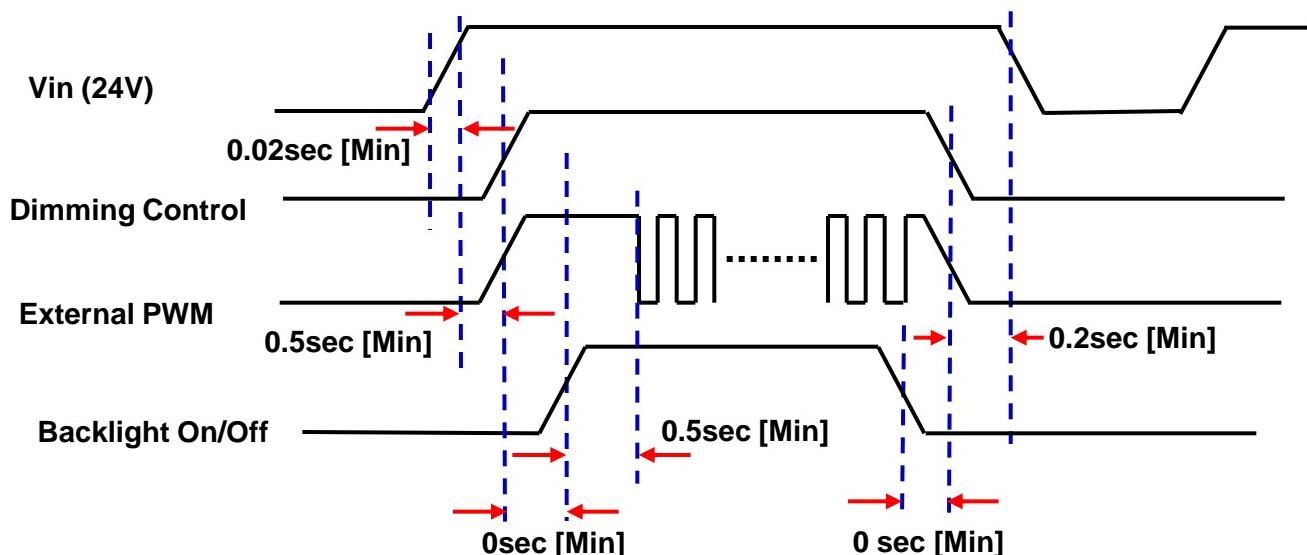
Note)   SEQUENCE :   On = Vin(24V) > Internal or External PWN ≥ Backlight On/off
                        OFF = Backlight On/Off ≥ Internal or External PWN > Vin(24V)

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4.4 LVDS Interface
- LVDS Receiver : T-con (merged)
- Data Format

	LVDS pin	JEIDA -DATA
TxOUT/RxIN0	TxIN/RxOUT0	R2
	TxIN/RxOUT1	R3
	TxIN/RxOUT2	R4
	TxIN/RxOUT3	R5
	TxIN/RxOUT4	R6
	TxIN/RxOUT6	R7
	TxIN/RxOUT7	G2
TxOUT/RxIN1	TxIN/RxOUT8	G3
	TxIN/RxOUT9	G4
	TxIN/RxOUT12	G5
	TxIN/RxOUT13	G6
	TxIN/RxOUT14	G7
	TxIN/RxOUT15	B2
	TxIN/RxOUT18	B3
TxOUT/RxIN2	TxIN/RxOUT19	B4
	TxIN/RxOUT20	B5
	TxIN/RxOUT21	B6
	TxIN/RxOUT22	B7
	TxIN/RxOUT24	HSYNC
	TxIN/RxOUT25	VSYNC
	TxIN/RxOUT26	DEN
TxOUT/RxIN3	TxIN/RxOUT27	R0
	TxIN/RxOUT5	R1
	TxIN/RxOUT10	G0
	TxIN/RxOUT11	G1
	TxIN/RxOUT16	B0
	TxIN/RxOUT17	B1
	TxIN/RxOUT23	RESERVED

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4.5 Input Signals, Basic Display Colors and Gray Scale of Each Color

COLOR	DISPLAY (8bit)	DATA SIGNAL																								GRAY SCALE LEVEL
		RED								GREEN								BLUE								
		R0	R1	R2	R3	R4	R5	R6	R7	G0	G1	G2	G3	G4	G5	G6	G7	B0	B1	B2	B3	B4	B5	B6	B7	
BASIC COLOR	BLACK	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	-
	BLUE	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1	-
	GREEN	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	-
	CYAN	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	-
	RED	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	-
	MAGENTA	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1	-
	YELLOW	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	-
	WHITE	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	-
GRAY SCALE OF RED	BLACK	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	R0
	DARK ↑	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	R1
		0	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	R2
		:	:	:	:	:	:			:	:	:	:	:	:			:	:	:	:	:	:			R3~ R252
	↓	:	:	:	:	:	:			:	:	:	:	:	:			:	:	:	:	:	:			
	LIGHT	1	0	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	R253
		0	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	R254
	RED	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	R255
GRAY SCALE OF GREEN	BLACK	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	G0
	DARK ↑	0	0	0	0	0	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	G1
		0	0	0	0	0	0	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	G2
		:	:	:	:	:	:			:	:	:	:	:	:			:	:	:	:	:	:			G3~ G252
	↓	:	:	:	:	:	:			:	:	:	:	:	:			:	:	:	:	:	:			
	LIGHT	0	0	0	0	0	0	0	0	1	0	1	1	1	1	1	1	0	0	0	0	0	0	0	0	G253
		0	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	G254
	GREEN	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	G255
GRAY SCALE OF BLUE	BLACK	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	B0
	DARK ↑	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	0	0	0	0	0	0	0	B1
		0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	0	0	0	0	0	0	B2
		:	:	:	:	:	:			:	:	:	:	:	:			:	:	:	:	:	:			B3~ B252
	↓	:	:	:	:	:	:			:	:	:	:	:	:			:	:	:	:	:	:			
	LIGHT	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	0	1	1	1	1	1	1	B253
		0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	B254
	BLUE	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1	B255

Note) Definition of Gray :
Rn : Red Gray, Gn : Green Gray, Bn : Blue Gray (n = Gray level)
Input Signal : 0 = Low level voltage, 1 = High level voltage

5. Interface Timing

5.1 Timing Parameters (DE mode)

SIGNAL	ITEM	SYMBOL	MIN.	TYP.	MAX.	Unit	NOTE
Clock	Frequency	$1/T_C$	130	148.5	160	MHz	-
Hsync		F_H	60	67.5	70	KHz	-
Vsync		F_V	48	60	65	Hz	-
Vertical Display Term	Active Display Period	T_{VD}	-	1080	-	Lines	-
	Vertical Total	T_V	1092	1125	1380	Lines	-
Horizontal Display Term	Active Display Period	T_{HD}	-	1920	-	Clocks	-
	Horizontal Total	T_H	2090	2200	2350	clocks	-

Note) This product have to receive the input of Hsync & Vsync signal
    (1) Test Point : TTL control signal and CLK at LVDS Tx input terminal in system
    (2) Internal V_{DD} = 3.3V
    (3) Spread spectrum
       - Modulation rate (max) : $\pm$ 1.5 %
       - Modulation Frequency : under 100KHz

5.2 LVDS Input Data Characteristics

| ITEM		SYMBOL	Min.	Typ.	Max.	UNIT	NOTE
Input Data Position	F_{IN}=80MHz	t_{RSRM}	-	-	450	ps	
		t_{RSLM}	-450	-	-	ps	
Input common mode voltage		V_{CM}	0.2	-	2.0	V	-
Differential Input Voltage		$|V_{ID}|$	100	-	600	mV	-

Note) When the skew is measured the Spread Spectrum should be 0%

5.3 Timing diagrams of interface signal (DE only mode)

T_V

T_{VD}

DE

T_H

T_{HD}

DE

D_{CLK}

T_C

DATA
SIGNALS

T_C

T_{CH}　T_{Cl}

D_{CLK}　0.5 V_{CC}

T_{DS}　T_{DH}

DISPLAY
DATA　0.5 V_{CC}

T_{ES}

DE　0.5 V_{CC}

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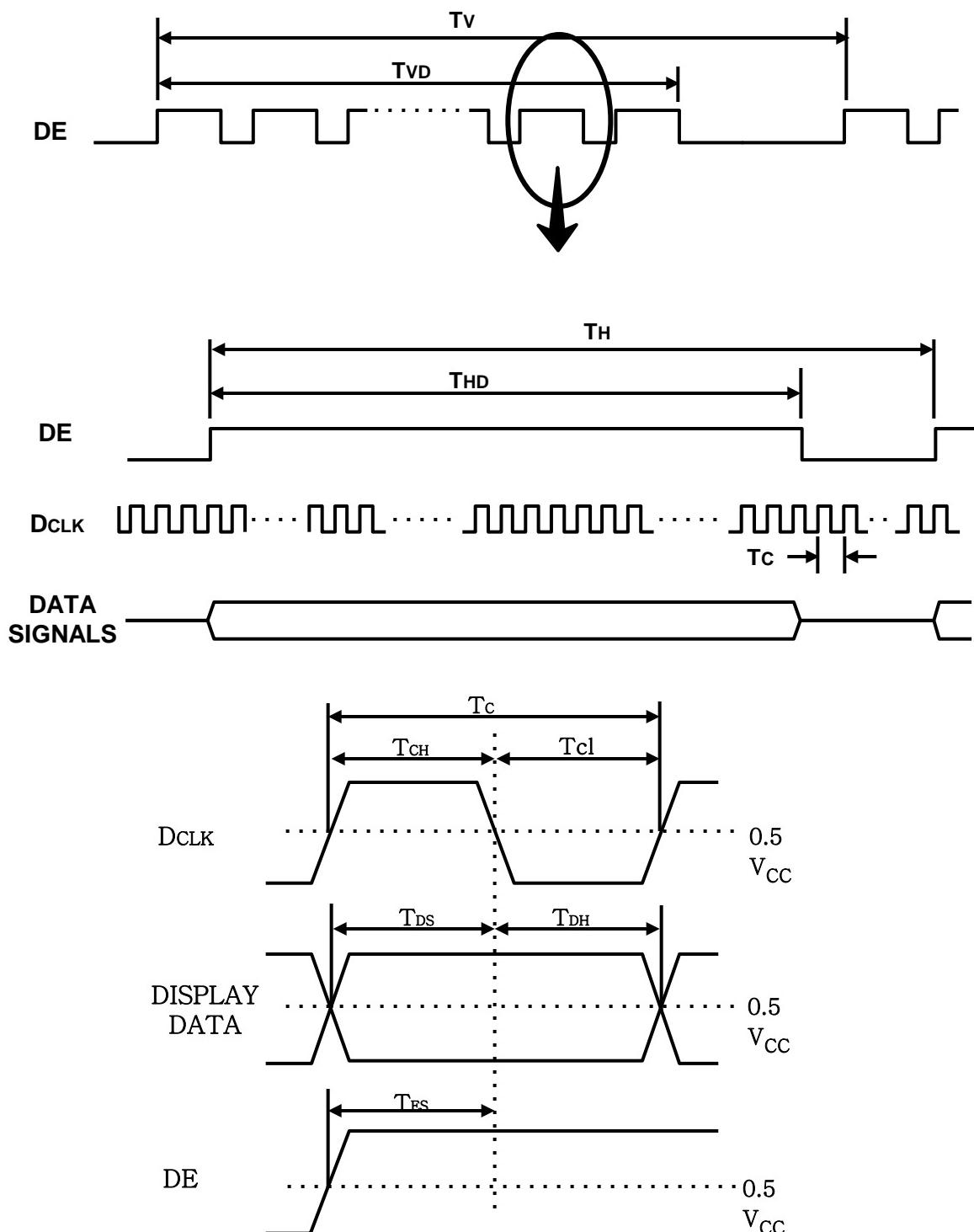
5.4 Power ON/OFF Sequence

To prevent a latch-up or DC operation of the LCD Module, the power on/off sequence should be as the diagram below.
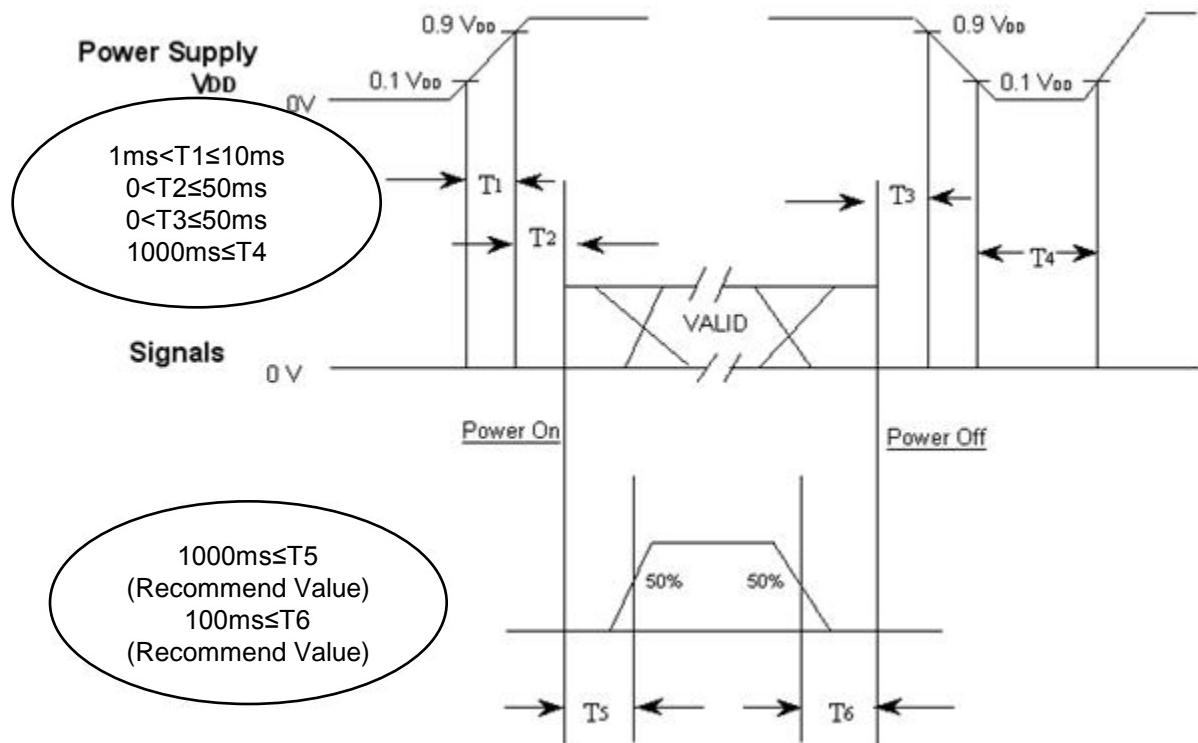
T1 : V_{DD} rising time from 10% to 90%

T2 : The time from V_{DD} to valid data at power ON.

T3 : The time from valid data off to V_{DD} off at power Off.

T4 : V_{DD} off time for Windows restart

T5 : The time from valid data to B/L enable at power ON.

T6 : The time from valid data off to B/L disable at power Off.

- The supply voltage of the external system for the Module input should be the same as the definition of V_{DD}.
- Apply the lamp voltage within the LCD operation range. When the back light turns on before the LCD operation or the LCD turns off before the back light turns off, the display may momentarily show abnormal screen.
- In case of V_{DD} = off level,
  please keep the level of input signals low or keep a high impedance.
- T4 should be measured after the Module has been fully discharged between power off and on period.
- Interface signal should not be kept at high impedance when the power is on.

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ACTIVE CENTER

2-4.44(ACTIVE ~ BEZEL OPEN)

1078.6±1.0 (OUTSIZE)

1062.6 ± 1.0 (BEZEL OPEN)

1054.08(ACTIVE AREA)

626.0±1.0 (OUTSIZE)

601.8±1.0 (BEZEL OPEN)

592.92 (ACTIVE AREA)

2-4.26(ACTIVE ~ BEZEL OPEN)

539.3

※ NOTE
1. BACKLIGHT : WLED
2. CONNECTOR SPECIFICATION
   - POWER CONNECTOR
     >. MAKER : YEONHO
     >. PART NO. : 20022WR-14B1
   - LVDS INPUT CONNECTOR(51PIN)
     >. MAKER : JAE/UJU
     >. PART NO. : FI-RE51S-HF
3. USERHOLE
   - TAP SIZE & TOLERANCE DESIGNED FOR GENERAL PURPOSE METRIC
     SCREW THREAD ACCORDING TO GENERAL GRADE OF 'KS B 0211'
   - ALLOWD SCREW TORQUE
     >. M3 TAP : 8.0 kgf.cm MAX
     >. M4 TAP : 13.0 kgf.cm MAX
     >. M8 TAP : 25.0 kgf.cm MAX
   - REPEATED INSERTION WARRANTY : 5TIMES(SCREW)
4. GAP BETWEEN TOP CHASSIS AND GLASS IS 1.9mm(MAX).
5. WEIGHT : ※※,000g(MAX)
6. BLACK MATRIX SPEC
   - |A-B| ≤ 2.0 m
   - |C-D| ≤ 2.0 m
7. TOLERANCE IN Z AXIS
   - Zone A : +2 /-1
   - Zone B : +1 /-1

BLACK MATRIX
ZONE A
20 mm

NO	PART NAME	CODE NO	SPECIFICATION	Q'TY	WEIGHT FINISH MATERIAL	APPLIED BY	REMARK
	OUTLINE DIMENSION		FRONT				

M3 TAP, H = 5.4

M3 TAP H= 7.5

M3 TAP, H=4.3

M4 TAP H= 4.3

2-M3 STUD

4-M6 STUD, H=8.4

2-M3 STUD(2 STEP), H= 7.5/8.5

M3 TAP, H: 3.9

M3 STUD(2 STEP), H=4.9/5.9

4-M3 STUD, H=11.0

△ M3 TAP POINT W/O EMBO : MAX DEPTH 3.5 mm

○ M3 TAP POINT W/O EMBO : MAX DEPTH 4.9 mm

☐ SCREW PROJECTION POINT :

GENERAL TOLERANCE			
STEP	LEVEL 1	LEVEL 2	LEVEL 3
0 < X < 4	±0.05	±0.1	±0.2
4 < X < 16	±0.08	±0.15	±0.3
16 < X < 64	±0.12	±0.25	±0.5
64 < X < 256	±0.25	±0.4	±0.8

SAMSUNG ELECTRONICS

MODEL NAME: LTA480HN01

PART/SHEET NAME: OUTLINE DIMENSION   SHEET 2/2

CODE NO.   REAR   VER. 000

7. PACKING

7.1 CARTON (Internal Package)
 (1) Packing Form
   Corrugated fiberboard box and corrugated cardboard as shock absorber
 (2) Packing Method

LCD Module

→ Direction to open

Packing Box

Pallet Plastic
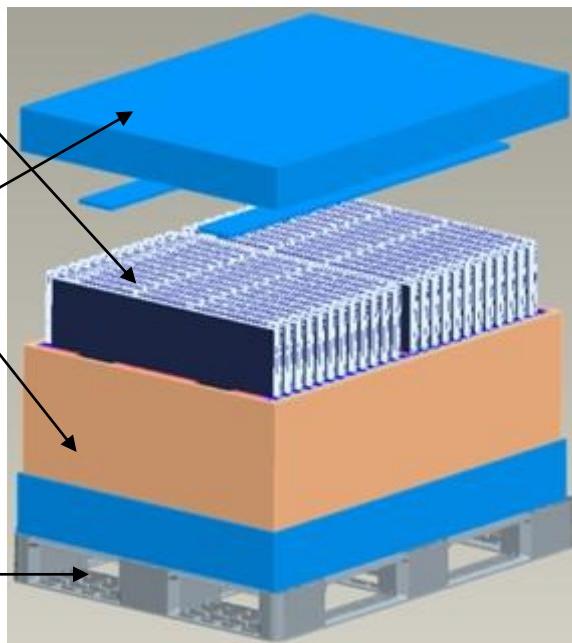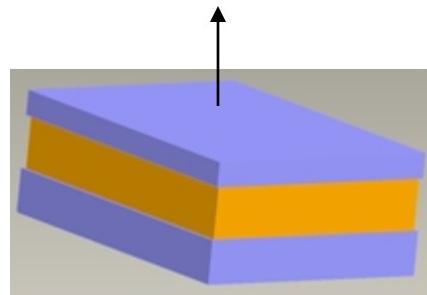
7.2 Packing Specification

Item	Specification	Remark
LCD Packing	22 ea / (Packing-Pallet Box)	1. 12.2kg / LCD (22ea) 2. Packing-Set(1ea) : 25.5Kg 3. Packing Material : Paper
Pallet	1Box / Pallet	1. Pallet weight = 7.8 kg
Packing Direction	Vertical	
Total Pallet Size	H x V x height	1270mm(H) x 1150mm(V) x 807mm(height)
Total Pallet Weight	303.02 Kg	Pallet (7.8Kg) + Module (268.4Kg) + Packing set (25.5Kg) + Desiccant (1.32Kg)

8. MARKING & OTHERS

A nameplate bearing followed by is affixed to a shipped product at the specified location on each product.

(1) Part number : LTA480HN01
(2) Revision: Three letters
(3) Lot number : X X X X XXX XX X

- Cell Position No. (In the Glass)
- Glass No. (In the one Lot)
- Lot No. (Glass)
- Month
- Year (Note1)
- Product code
- Line

(4) Nameplate Indication

CH U®us
LEAD-FREE
SAMSUNG
LTA480HN01
0529
XXXXXXXXX C01
MADE IN KOREA

Week code : 05 29
— week
— year

40mm

Revision code

Lot number

80mm

(5) Packing box attach

DVC : **LTA480HN01**
TYPE: **XXXX**
QTY : **22**

ZAT53K0003   G1

100mm

Part number

Box serial number

165mm
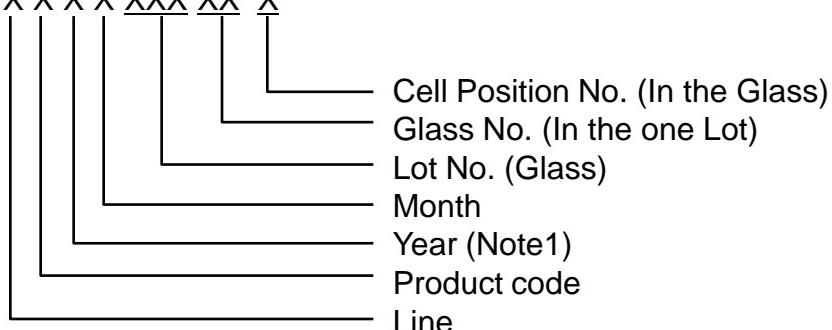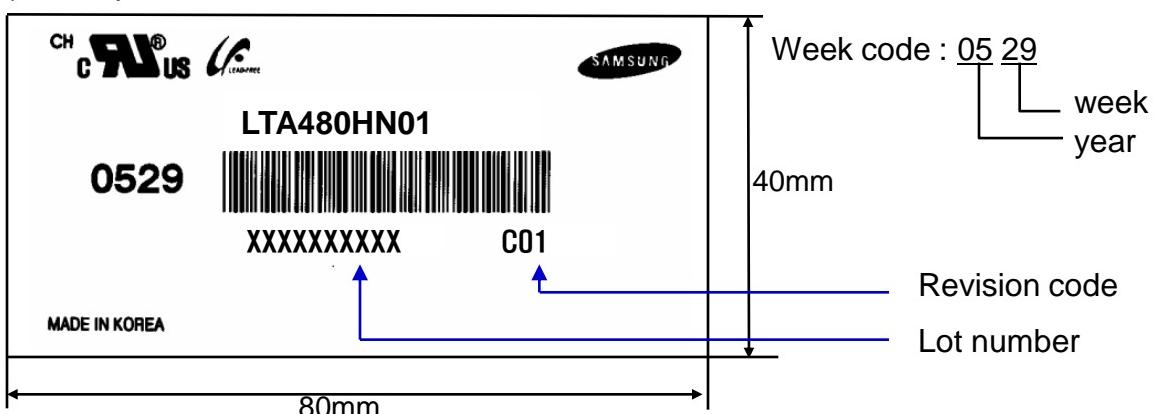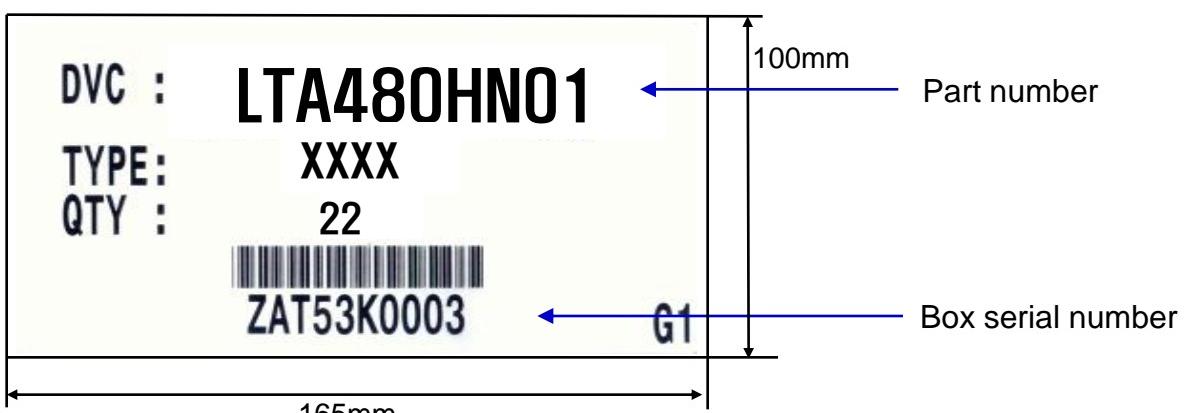
(6) Others
1. After service part
   Lamps cannot be replaced because of the narrow bezel structure.

9. General Precautions

9.1 Handling

(a) When the Module is assembled, it should be attached to the system firmly using all mounting holes. Be careful not to twist and bend the Module.

(b) Because the converter use high voltage, it should be disconnected from power before it is assembled or disassembled.

(c) Refrain from strong mechanical shock and / or any force to the Module. In addition to damage, this may cause improper operation or damage to the Module and LED back light.

(d) Note that polarizers are very fragile and could be damage easily. Do not press or scratch the surface harder than a HB pencil lead.

(e) Wipe off water droplets or oil immediately. If you leave the droplets for a long time, staining or discoloration may occur.

(f) If the surface of the polarizer is dirty, clean it using absorbent cotton or soft cloth.

(g) Desirable cleaners are water, IPA (Isopropyl Alcohol) or Hexane. Do not use ketone type materials (ex. Acetone), Ethyl alcohol, Toluene, Ethyl acid or Methyl chloride. It might permanent damage to the polarizer due to chemical reaction.

(h) If the liquid crystal material leaks from the panel, it should be kept away from the eyes or mouth . In case of contact with hands, legs or clothes, it must be washed away with soap thoroughly.

(i) Protect the module from Electrostatic discharge. Otherwise the ASIC IC or Semiconductor would be damaged.

(j) Use finger-stalls with soft gloves in order to keep display clean during the incoming inspection and assembly process.

(k) Do not disassemble the Module.

(l) Do not disassemble shield case of converter& C-PBA

(m) Do not connect N.C pins. (Samsung internal use only)

(n) Protection film for polarizer on the Module should be slowly peeled off just before use so that the electrostatic charge can be minimized. Must put on antistatic glove while handle a module

(o) Pins of I/F connector should not be touched directly with bare hands.

9.2 Storage

We highly recommend to comply with the criteria in the table below.

ITEM	UNIT	Min.	Max.
Storage Temperature	(℃)	5	40
Storage Humidity	(%rH)	35	75
Storage Life	12 months		
Storage Condition	- The storage room should provide good ventilation and temperature control. - Products should not be placed on the floor, but on the Pallet away from a wall. - Prevent products from direct sunlight, moisture nor water; Be cautious of a build up of condensation. - Avoid other hazardous environment while storing goods. - If products delivered or kept in conditions of over the storage period of 3months, the recommended temperature or humidity range, we recommend you leave them at a temperature of 20℃ and a humidity of 50% for 24 hours.		

9.3 Operation

(a) Do not connect or disconnect the Module in the "Power On" condition.

(b) Power supply should always be turned on/off by the "Power on/off sequence"

(c) Module has high frequency circuits. Sufficient suppression to the electromagnetic interference should be done by system manufacturers. Grounding and shielding methods may be important to minimize the interference.

(d) The cable between the back light connector and its Converter power supply should be connected directly with a minimized length. A longer cable between the back light and the Converter may cause lower luminance of lamp(LED) and may require higher startup voltage(Vs).

9.4 Operation Condition Guide

(a) The LCD product should be operated under normal conditions.
Normal condition is defined as below;
- Temperature : 20±15℃
- Humidity : 55±20%
- Display pattern : continually changing pattern (Not stationary)

(b) If the product will be used in extreme conditions such as high temperature,
humidity, display patterns or operation time etc.., It is strongly recommended
to contact SEC for Application engineering advice. Otherwise, its reliability and
function may not be guaranteed. Extreme conditions are commonly found at
Airports, Transit Stations, Banks, Stock market, and Controlling systems.

9.5 Others

(a) Ultra-violet ray filter is necessary for outdoor operation.

(b) Avoid condensation of water. It may result in improper operation or disconnection
of electrode.

(c) Do not exceed the absolute maximum rating value. (supply voltage variation,
input voltage variation, variation in part contents and environmental temperature,
and so on)
Otherwise the Module may be damaged.

(d) If the Module keeps displaying the same pattern for a long period of time,
the image may be "sticked" to the screen.
To avoid image sticking, it is recommended to use a screen saver.

(e) This Module has its circuitry PCB's on the rear side and should be handled
carefully in order not to be stressed.

(f) Please contact SEC in advance when you display the same pattern for a long time.